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# U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10040920	FILING DATE 12/28/2001	CLASS 711	SUBCLASS 141	CAU 2485	EXAMINER <i>THA1</i>
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\*\*CONTINUING DATA VERIFIED:

\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		10559-566001 P12728
Verified and Acknowledged Examiners's initials		
TITLE : Method for extending the local memory address space of a processor		

U.S. DEPT. OF COMM./PAT. & TM.-PTO-426L (Rev. 12-92)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G
Assistant Examiner		DRAWING	
ISSUE FEE		Sheets Drwg.	Figs.Drwg.
Amount Due	Date Paid	Print Fig.	
Primary Examiner		Application Examiner	
PREPARED FOR ISSUE			
<input type="checkbox"/> TERMINAL DISCLAIMER		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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